FKCC8233

Dual N-Ch Fast Switching MOSFETs

★ Green Device Available
★ Super Low Gate Charge
★ Excellent CdV/dt effect decline
★ Advanced high cell density Trench technology

Product Summary

Green

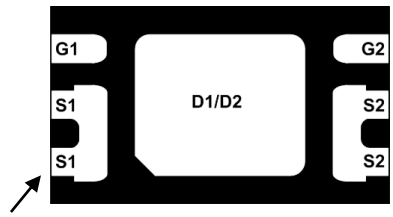
BVDSS	RDSON	ID
20V	7.2mΩ	11A

General Description

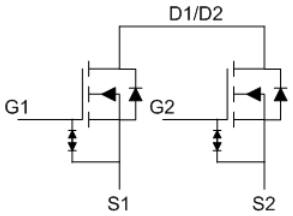
The FKCC8233 is the highest performance trench N-ch MOSFETs with extreme high cell density, which provide excellent RDSON and gate charge for most of the small power switching and load switch applications.

The FKCC8233 meet the RoHS and Green Product requirement with full function reliability approved.

DFN2x3 Pin Configuration



Pin1



Absolute Maximum Ratings

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	20	V
V_{GS}	Gate-Source Voltage	±12	V
I_D@T_A=25℃	Continuous Drain Current, V_{GS} @ 4.5V[1]	11	A
I_D@T_A=70℃	Continuous Drain Current, V_{GS} @ 4.5V[1]	8.8	A
I_{DM}	Pulsed Drain Current[2]	70	A
P_D@T_A=25℃	Total Power Dissipation[1]	1.56	W
T_{STG}	Storage Temperature Range	-55 to 150	℃
T_J	Operating Junction Temperature Range	-55 to 150	℃

Thermal Data

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction-Ambient [1] (t ≤10s)	---	80	℃/W

N-Channel Electrical Characteristics (T_J=25 ˚C, unless otherwise noted)

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	V_{GS}=0V , I_D=250uA	20	---	---	V
$R_{DS(ON)}$	Static Drain-Source On-Resistance[2]	V_{GS}=4.5V , I_D=5.5A	4.5	6	7.2	mΩ
		V_{GS}=4.0V , I_D=5.5A	4.8	6.2	7.5	
		V_{GS}=3.7V , I_D=5.5A	5.0	6.5	8.2	
		V_{GS}=3.1V , I_D=5.5A	5.3	7	9	
		V_{GS}=2.5V , I_D=5.5A	6	8.2	10.2	
$V_{GS(th)}$	Gate Threshold Voltage	V_{GS}=V_{DS} , I_D =250uA	0.5	---	1.5	V
I_{DSS}	Drain-Source Leakage Current	V_{DS}=18V , V_{GS}=0V , T_J=25˚C	---	---	1	uA
		V_{DS}=18V , V_{GS}=0V , T_J=55˚C	---	---	5	
I_{GSS}	Gate-Source Leakage Current	V_{GS}=±12V , V_{DS}=0V	---	---	±10	uA
gfs	Forward Transconductance	V_{DS}=5V , I_D=5.5A	---	38	---	S
Q_g	Total Gate Charge (4.5V)	V_{DS}=16V , V_{GS}=4.5V , I_D=10A	---	23	---	nC
Q_{gs}	Gate-Source Charge		---	3.5	---	
Q_{gd}	Gate-Drain Charge		---	8.4	---	
$T_{d(on)}$	Turn-On Delay Time	V_{DD}=16V , V_{GS}=4.5V , R_G=6Ω I_D=5.5A	---	10.2	---	ns
T_r	Rise Time		---	41	---	
$T_{d(off)}$	Turn-Off Delay Time		---	67	---	
T_f	Fall Time		---	31	---	
C_{iss}	Input Capacitance	V_{DS}=10V , V_{GS}=0V , f=1MHz	---	1767	---	pF
C_{oss}	Output Capacitance		---	184	---	
C_{rss}	Reverse Transfer Capacitance		---	155	---	

Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_S	Continuous Source Current[1]	V_G=V_D=0V , Force Current	---	---	11	A
I_{SM}	Pulsed Source Current[2]		---	---	70	A
V_{SD}	Diode Forward Voltage[2]	V_{GS}=0V , I_S=11A , T_J=25˚C	---	---	1.2	V

Note :
1.The data tested by surface mounted on a 1 inch^2 FR-4 board with 2OZ copper, t ≤10s.
2.The data tested by pulsed , pulse width ≦ 10us , duty cycle ≦ 1%

Typical Characteristics

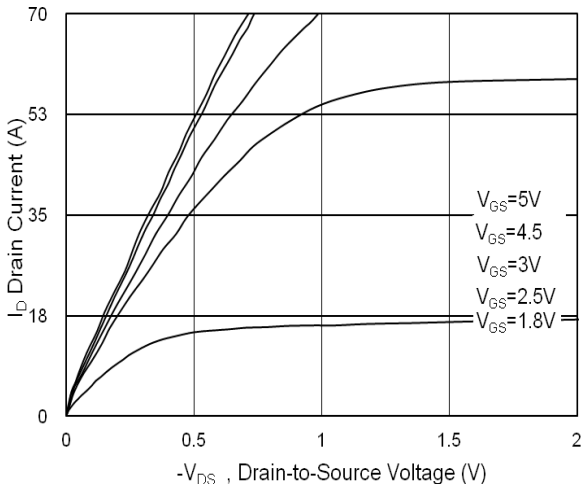


Fig.1 Typical Output Characteristics

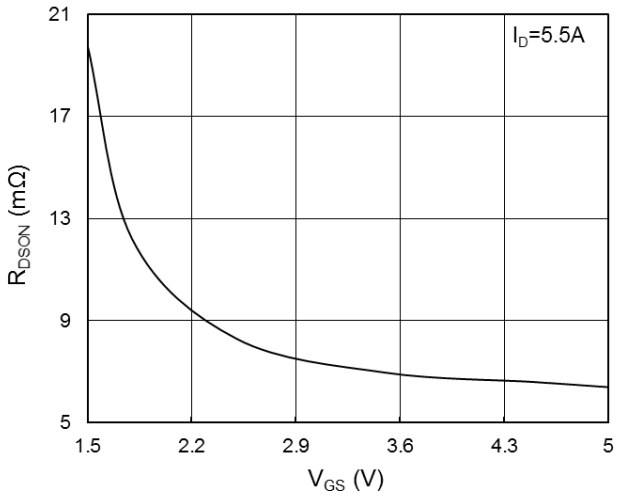


Fig.2 On-Resistance vs. Gate-Source

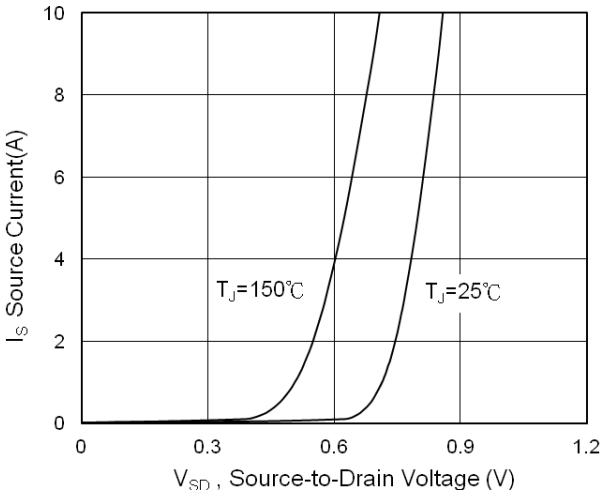


Fig.3 Forward Characteristics of Reverse

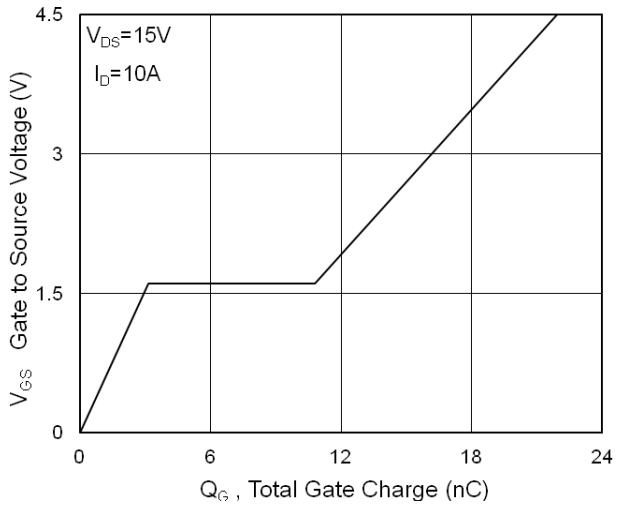


Fig.4 Gate-Charge Characteristics

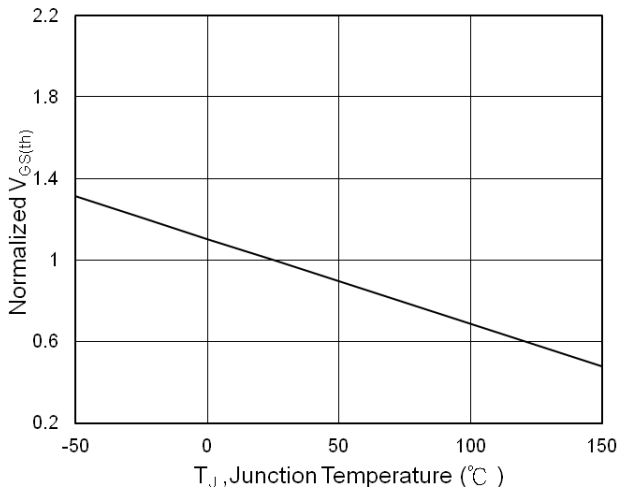


Fig.5 $V_{GS(th)}$ vs. T_J

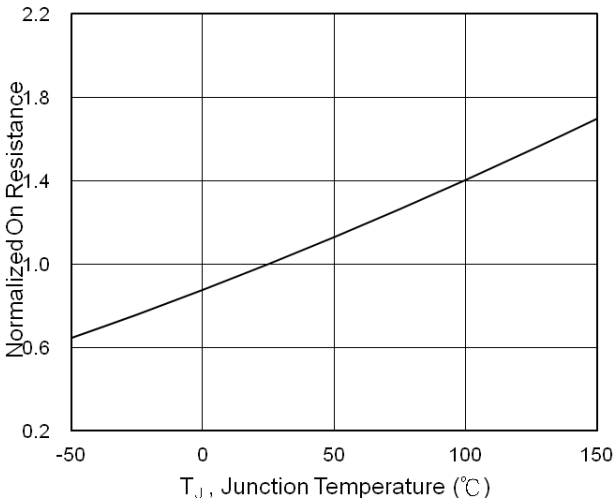


Fig.6 Normalized R_{DSON} vs. T_J

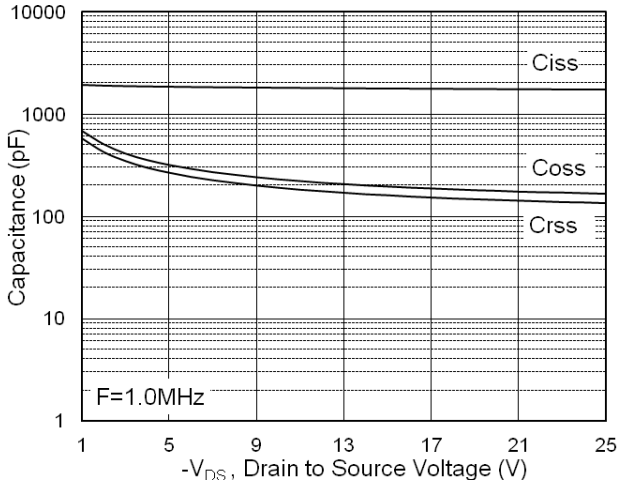


Fig.7 Capacitance

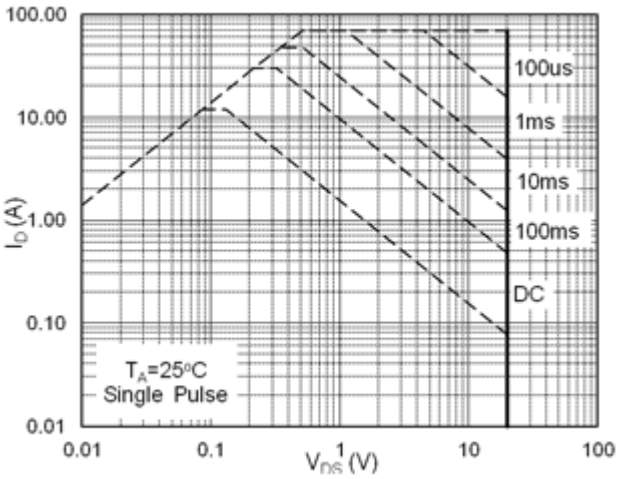


Fig.8 Safe Operating Area

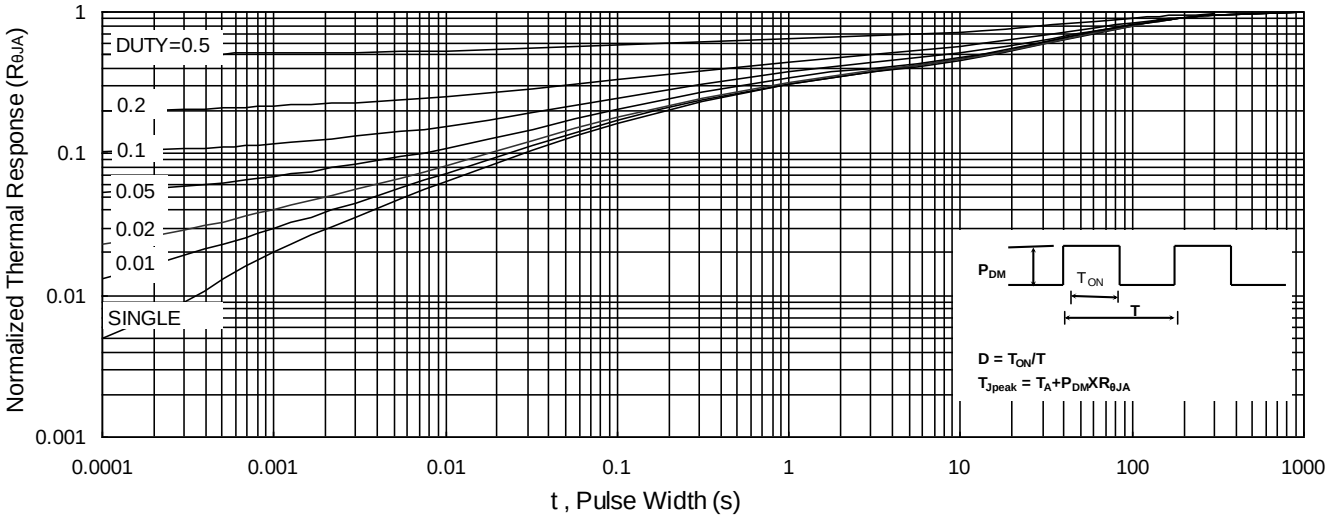


Fig.9 Normalized Maximum Transient Thermal Impedance

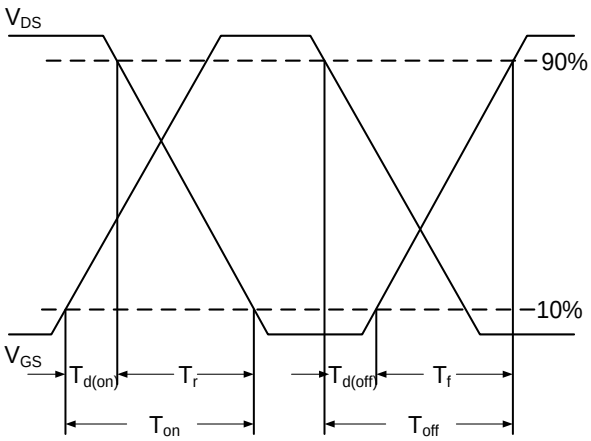


Fig.10 Switching Time Waveform

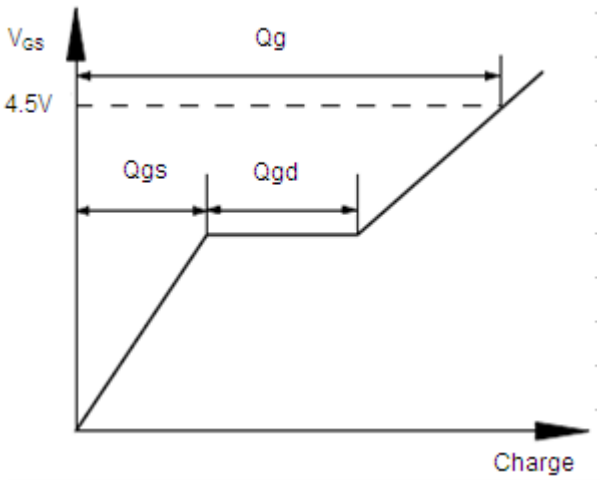


Fig.11 Gate Charge Waveform